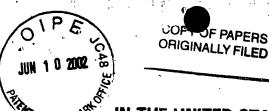
#1210



PATENT APPLICATION 042390.P3275RC1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

III LA	application of:	) May 30, 2002
Но	rden et. al	) )
Seria	l No.: 09/668,952	) Group Art Unit: 2181
Filed:	September 21, 2000	) Examiner: R. Dharia RECEIVED
For:	METHOD AND APPARATUS PROVIDING VOLTAGES AND FREQUENCIES SELECTION REAL TIME CRITERIA TO CONTRO	IG MULTIPLE
	I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE IN ACCORDANCE WITH 37 CFR §1.10 AS EXPRESS MAIL IN AN ENVELOPE ADDRESSED TO:	
	HONORABLE DIRECTOR OF THE UNITED SOFFICE, WASHINGTON, D.C. 20231, ON: May	•

HONORABLE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE, Washington, D.C. 20231

SIR:

In response to the Advisory Action mailed May 30, 2002, Applicants are filing the enclosed Request for Continued Examination (RCE) Application under 37 CFR § 1.114.

Please enter the following amendment prior to examination so that there is a total of ten (10) independent and a total of thirty-seven (37) claims pending in the application.

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042390.P3275RC1

## **AMENDMENT**

## Amendment to Specification

Please insert the following as the first sentence of the specification:

"This is a continuation of Application No. 09/668,952 filed September 22, 2000."

## Amendment to Claims

Please amend the claims as shown below and add claims 32-37.

- 7. (Twice amended) An apparatus, comprising:
- a static random access memory;
- a processor coupled to the static random access memory;
- a voltage regulator adapted to provide at least two voltage potential levels to at least a portion of the processor; and

wherein the voltage potential level provided by the voltage regulator is adapted to be adjusted depending on <u>a frequency corresponding to</u> the operational load of the processor.

- 14. (Twice amended) A method of operating a processor, comprising:

  monitoring an application mix executed by a processor; and
  adjusting the voltage potential level provided to at least a portion of the
  processor based on the application mix executed by the processor.
- 17. (Twice amended) An article comprising: a storage medium having stored thereon instructions, that, when executed by a computing platform, results in:

monitoring an application mix executed by a processor; and